

10/052,853

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,849,896 B2
DATED : February 1, 2005
INVENTOR(S) : Wada et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page.

Item [57] **ABSTRACT**,

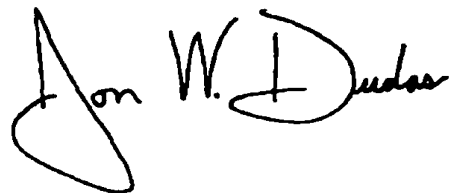
Line 3, after "semiconductor", insert -- substrate --.

Line 4, after "a", insert -- conductive layer on --.

Line 7, delete "no" and insert -- not --.

Signed and Sealed this

Seventeenth Day of May, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office